

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Terence Edwin Dodgson

Group Art Unit: 2121

Serial No.: 10/787,373

Docket: 678-1388

Filed:

February 25, 2004

Dated: June 10, 2004

For:

NEURAL NETWORKS DECODER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to Applicant's duty of disclosure, the information listed in the attached form PTO-1449 is brought to the attention of the Examiner. A copy of each reference is attached hereto.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that the references are prior art. The items listed are submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of these particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 10, 2004.

Dated: June 10, 2004

Paul I. Farrell

The filing of this Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

To the best of Applicant's knowledge, this Information Disclosure Statement is being filed before the date of mailing of a first Office Action in connection with this case.

The claims of the application as now presented are believed to patentably distinguish over the prior art and to be in condition for allowance. Early and favorable consideration of the case is respectfully requested.

Respectfully submitted,

Paul JVFarrell

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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)											
		1.	D.E. Rumelhart et al., "Learning Internal Representations by Error Propagation", Parallel Distributed Processing, Explorations in the Microstructure of Cognition, Vol. 1: Foundations, Chapter 8, 1986.								
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